

**YEF
ECE
2019**

3rd International Young Engineers Forum on Electrical and Computer Engineering Call for Papers

May 10, 2019
Caparica (Lisbon), Portugal

Important Dates:

Submission of Abstract:

January 11, 2019

Full paper submission:

February 28, 2019

Paper acceptance notification:

March 29, 2019

Final paper submission:

April 12, 2019

YEF-ECE 2019

3rd International Young Engineers Forum on Electrical and Computer Engineering

*Following the success of the 2018 edition (papers are available on the **IEEE Xplore digital library**) we are proud to announce the organization of the 3rd International Young Engineers Forum on Electrical and Computer Engineering.*

Scope: Electrical engineers apply electrical and electronic theory to obtain solutions for problems related to the development, design and operation of electrical hardware and software, control systems, electrical machines and communications systems. Computer engineers are concerned with the design, development, and implementation of new and challenging computer technology in a myriad of consumer, industrial, commercial, and military applications. Besides development, design, operations, and research, electrical and computer engineers are typically involved in the manufacture, installation, and maintenance of computational devices, electrical and electronic equipment and systems employed by a wide variety of organizations which produce, use or provide services to such equipment, and ranging from tiny electronic devices to large complex systems.

The International Young Engineers Forum looks for the latest developments and innovative applications in electrical and computer engineering, dealing with systems' design and utilization, looking forward to efficient devices and systems with appropriate control algorithms to meet the needs of business and industry in a global economy. This event will be a unique opportunity for young engineers to connect with each other enabling experience's sharing and to become internationally active.

Young engineers / graduating MSc students are encouraged to submit manuscripts with their latest innovative developments in Electrical and Computer Engineering, based on their **MSc theses** or **early career projects**.

Proceedings will be submitted to be published in IEEE Xplore

Each submitted paper shall be jointly co-authored by a young engineer and his/her supervisor or mentor. The YEF-ECE Program Committee will review all submitted **full papers** and will select the best for presentation based on their innovative and original contribution to engineering solutions. Evaluation of papers is double-blinded and based on the full text, considering the original scientific and technological contribution.

Prospective authors should submit a short abstract to the conference in advance, in order to check if the proposed topic fits within the conference scope.

YEF-ECE 2019 will be co-located with the doctoral conference DoCEIS 2019, giving participants the opportunity to attend both events on the same day.

Conference Location: The Conference will be held in Caparica (Lisbon region). Lisbon, the capital of Portugal with a population of one million inhabitants, is a modern city, which has a lot to offer both in terms of history, culture and social life. It stands on the westernmost point of land of the European continent, where the Tagus river flows into the Atlantic Ocean. Its Atlantic climate, with some Mediterranean influences, is probably the mildest of all European capitals.

Organizational Co-Sponsorship:



Technical Sponsorship (requested):



<http://sites.uninova.pt/yef-ece>